

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): CHEN et al.

Serial No.: 10/054,373

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Title: Semiconductor Processing Chamber
Substrate Holder Method and
Structure

Attorney Docket No.: AIBT-I2K09

Group Art Unit: 2831

Examiner: Harris, A.

RESPONSE TO OFFICE ACTION

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

A. Introductory Comments

The following is submitted in response to the Office Action dated October 10, 2003, and
is currently due January 10, 2004.

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